



Sheet 1 of 3

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Attorney Docket No.: L&L-I0232	Applic. No. 10/047,013
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Applicant Wolfgang Rösner et al.	
		Filing Date January 16, 2002	Group Art Unit 2811

### U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
PG	A	5,714,766	02/03/98	Chen et al.			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

### FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
PG	J	196 32 835 C1	04/02/98	Germany				X
PG	K	0 843 361 A1	05/20/98	Europe			X	
	L							
	M							
	N							

### OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

PG	O	H.I. Liu et al.: "Self-limiting oxidation for fabricating sub-5 nm silicon nanowires", Appl. Phys. Lett., Vol. 64, No. 11, March 14, 1994, pp. 1383-1385
PG	P	Harald Gossner et al.: "Vertical Si-Metal-Oxide-Semiconductor Field Effect Transistors with Channel Lengths of 50 nm by Molecular Beam Epitaxy", Jpn. J. Appl. Phys., Vol. 33, 1994, pp. 2423-2428

EXAMINER	RAZI KPS	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;  
Draw line through citation if not in conformance and not considered. Include copy of this form with  
next communication to applicant.

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Sheet 2 of 3

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE  
STATEMENT BY APPLICANT  
(37 CFR 1.98(b))

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	J						
	K						
	L						
	M						
	N						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>RB</i>	O	Lothar Risch et al.: "Vertical MOS Transistors with 70 nm Channel Length", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1495-1498
<i>FB</i>	P	Hiroshi Fukuda et al.: "Fabrication of silicon nanopillars containing polycrystalline silicon/insulator multilayer structures", Appl. Phys. Lett., Vol. 70, No. 3, January 20, 1997, pp. 333-335

EXAMINER	<i>KPL</i>	DATE CONSIDERED
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	A						
	B						
	C						
	D						
	E						
	F						
	G						TC 260
	H						ALL P
	I						100-100

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

P6	O	Hiroshi Mizuta et al.: "High-speed single-electron memory: cell design and architecture", XP-002151823, IEEE 1998, pp. 67-72
	P	
EXAMINER	<i>PAUL TRABA</i>	DATE CONSIDERED <i>04/01/03</i>

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.